



2634 IFW

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	Filing Date	March 01, 2002
	First Named Inventor	Suresh M. Menon
	Examiner Name	Ted M. Wang
	Art Unit	2634
	Patent No.	
Attorney Docket Number		X-1050 US

ENCLOSURES (check all that apply)		
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X-1050 US
10/090,250

PATENT
Conf. No.: 4098

IN THE UNITED STATES PATENT OFFICE

Applicants: Suresh M. Menon et al.

Assignee: Xilinx, Inc.

Title: "HIGH SPEED CONFIGURABLE TRANSCEIVER ARCHITECTURE"

Serial No.: 10/090,250

File Date: 03/01/2002

Examiner: Ted M. Wang

Art Unit: 2634

Docket No.: X-1050 US

Conf. No.: 4098

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AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the Patent Office on September 20, 2005, please reconsider allowance of the claims in view of the Remarks below.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5.